Faculty Information

Personal Information

Name of the Faculty	Mr. Rushikesh Vinayak Dharmadhikari		
Designation	HOD	**Department**	Mechatronics
Email	rushikesh.dharmadhikari@pravara.in		

Qualification Details

Sr. No.	Exam Passed	Board /University	Year of Passing
01	P.G.(VLSI & Embedded System)	Savitribai Phule Pune University	2015
02	MBA Marketing	Pune University	2013
02	U.G. (Electronics & Telecommunication)	Pune University	2010
03	Diploma in Electronics & Telecommunication	MSBTE Mumbai, Maharashtra	2007
04	SSC	Pune Board	2004

Work Experience

Teaching Experience: 9 Yrs	Industrial Experience:00	Total Experience: 9 Yrs

Subject Taught till date

- Electrical Engineering
- Electrical Circuit & Network
- Management
- Environmental Studies
- Industrial Measurement

Membership with Professional Bodies

No

Paper Publications

Sr. No.	Title	Published Journal	Year of Publication
1	An Overview of Morphological Binary Image Processing using FPGA.	IJIFR	2015
2	An Implementation of Morphological Binary Image Processing using FPGA.	IJIFR	2015
3	Role of cloud computing in education.	Conference, P.Dr.V.V.P. College of Engineering, Viladghat, Ahmednagar (State Level)	2017
4	A Review on Internet of Things.	Conference ,Amrutvahini Engg. College Sangamner (National Level)	2017